

8. (Amended) The apparatus of claim [8] 7 further providing a means for signal level control so as to provide identical levels in the output and input signals.
9. (Amended) The apparatus of claim [8] 7 further providing a means for signal delay control so as to provide synchronization between the output and input signals.
11. (Amended) The apparatus of claim [11] 10 further providing a means for signal level control so as to provide identical levels in the output and input signals.
12. (Amended) The apparatus of claim [11] 10 further providing a means for signal delay control so as to provide synchronization between the output and input signals.
14. (Amended) The apparatus of claim [14] 13 further providing a means for signal level control so as to provide identical levels in the output and input signals.
15. (Amended) The apparatus of claim [14] 13 further providing a means for signal delay control so as to provide synchronization between the output and input signals.
16. (Amended) The apparatus of claim [14] 13 wherein the output low bandpass filtering means is enabled for passing 20-300 Hertz, and the frequency shifting means is enabled for shifting the 20-300 Hertz to 2.25-3 kilo-Hertz.
17. (Amended) The apparatus of claim [14] 13 wherein the output high bandpass filtering means is enabled for passing 3-20 kilo-Hertz, and the frequency dividing means is enabled for dividing by 10.
18. (Amended) The apparatus of claim [14] 13 wherein the input low bandpass filtering means is enabled for passing 2.25-3 kilo-Hertz, and the frequency shifting means is enabled for shifting the 2.25-3 kilo-Hertz to 20-300 Hertz.
19. (Amended) The apparatus of claim [14] 13 wherein the input high bandpass filtering means is enabled for passing 300-2000 Hertz, and the frequency multiplying means is enabled for multiplying by 10.



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